



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2182  
#34

In re application of: **Austen et al.**

Serial No.: **09/925,584**

Filed: **August 9, 2001**

**For: Method, System, and Product for  
Booting a Partition Using One of  
Multiple, Different Firmware Images**

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Group Art Unit: **2182**

Examiner: **Unknown**

Attorney Docket No.: **AUS920010565US1**

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

I hereby certify this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on September 20, 2001.

By:

Krista Douthitt  
Krista Douthitt

**TRANSMITTAL DOCUMENT**

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

ENCLOSED HEREWITH:

- Information Disclosure Statement;
- Form PTO-1449;
- References AA-AH; and
- Our return postcard.

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No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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Examiner: Unassigned

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Hon. Assistant Commissioner of Patents  
Washington, D.C. 20231

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Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

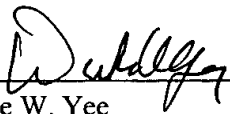
Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.

In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee is required.

Respectfully submitted,

Date: September 20, 2001

  
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Form PTO-1449

LIST OF PRIOR ART CITED  
BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.  
AUS920010565US1SERIAL NO.  
09/925,584APPLICANT **Austen et al.**FILING DATE **August 9, 2001**GROUP ART UNIT **2182**

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	AA 4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
	AB 4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
	AC 5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

## OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

AD	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", one page.
AE	IBM Technical Disclosure Bulletin, Vol. 38, No. 04, April 1995, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", p. 325.
AF	IBM Technical Disclosure Bulletin, Vol. 36, No. 03, March 1993, "Sharing Read-Only Memory among Multiple Logical Partitions", pp. 303-304.
AG	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", 2 pages.
AH	IBM Technical Disclosure Bulletin, Vol. 39, No. 06, June 1996, "Coordinating Multiple Server Partitions to Enter Power-Save State", pp. 235-239.

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.